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Introduction to VLSI CMOS Circuits Design 1 Carlos Silva Cardenas Catholic University of Peru` Takeo Yoshida University of the Ryukyus Alberto Palacios Pawlovsky Toin University of Yokohama August 18, 2006 1Work supported by a grant of the Ministry of Education and Science of Japan and the Toin University of Yokohama.

Introduction to VLSI CMOS Circuits Design 1

Significance to digital electronics. In Very-Large-Scale Integration (VLSI) integrated circuit microprocessor design and semiconductor fabrication, a process corner represents a three or six sigma variation from nominal doping concentrations (and other parameters) in transistors on a silicon wafer.This variation can cause significant changes in the duty cycle and slew rate of digital signals ...

Process corners - Wikipedia

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